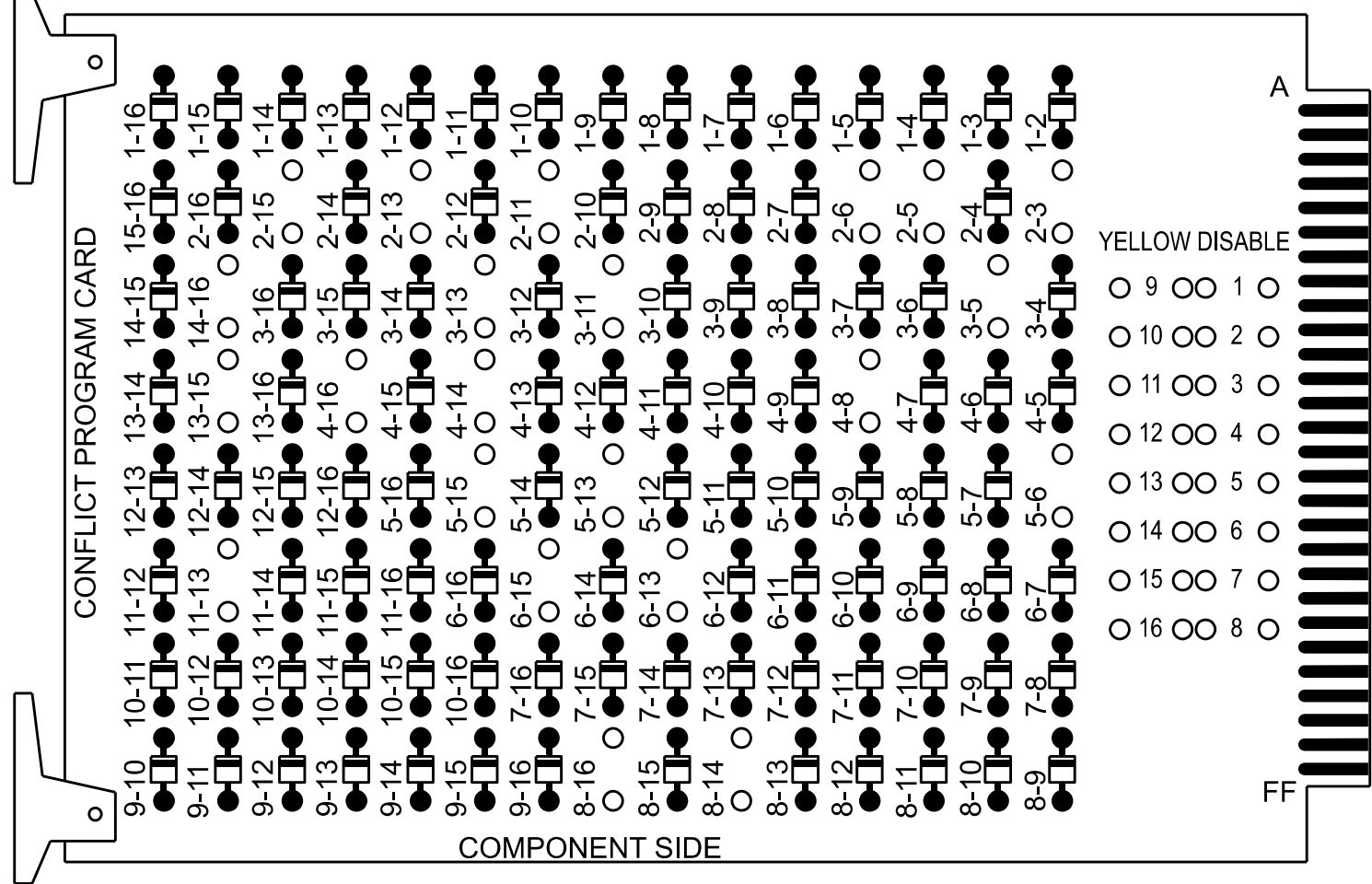


16 CHANNEL CONFLICT MONITOR
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

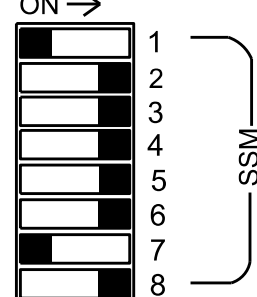
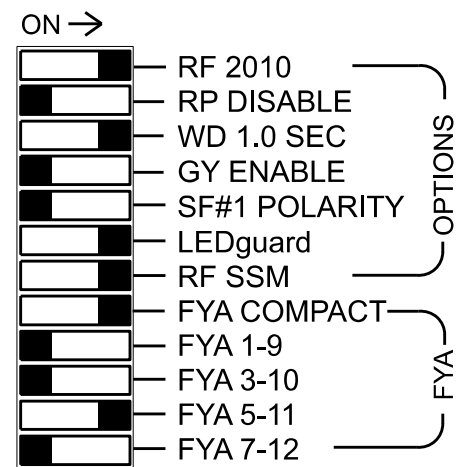
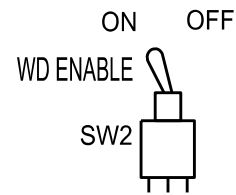
REMOVE DIODE JUMPERS 2-3, 2-5, 2-6, 2-11, 2-13, 2-15, 3-5, 3-11, 3-13, 4-8, 4-14, 4-16, 5-6, 5-13, 5-15, 6-13, 6-15, 8-14, 8-16, 11-13, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.
- Special cabinet wiring is required to utilize FYA COMPACT mode. See Ped Yellow Conflict Monitor Wiring Detail on sheet 2.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
- Program phases 4 and 8 for Dual Entry.
- Program phases 2, 4, 6 and 8 for Simultaneous Start.
- Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.

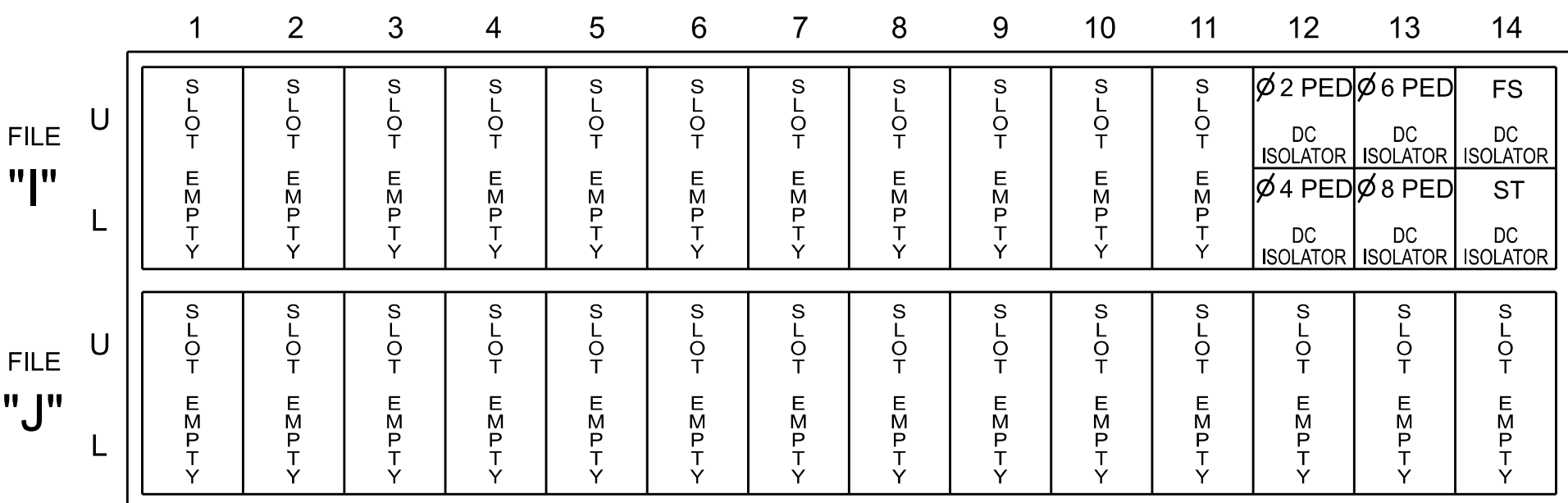
EQUIPMENT INFORMATION

Controller.....2070LX
Cabinet.....332
Software.....Q-Free MAXTIME
Cabinet Mount.....Base
Output File Positions.....12
Load Switches Used.....S2, S2P, S3, S4, S4P, S5, S6, S6P, S8, S8P
Phases Used.....2, 2PED, 4, 4PED, 5, 6, 6PED, 7, 8, 8PED
Overlap "1".....Not Used
Overlap "2".....Not Used
Overlap "3".....*
Overlap "4".....Not Used
Overlap "5".....*

*See overlap programming detail on sheet 2

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

! If present, remove jumper from J1-W to I4-W on rear of input file.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
PED PUSH BUTTONS												
P21,P22	TB8-4,6	I12U	67	33	2	PED 2						
P41,P42	TB8-5,6	I12L	69	35	4	PED 4						
P61,P62	TB8-7,9	I13U	68	34	6	PED 6						
P81,P82	TB8-8,9	I13L	70	36	8	PED 8						

NOTE:
INSTALL DC ISOLATORS
IN INPUT FILE SLOTS
I12 AND I13.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

SIGNAL HEAD HOOK-UP CHART

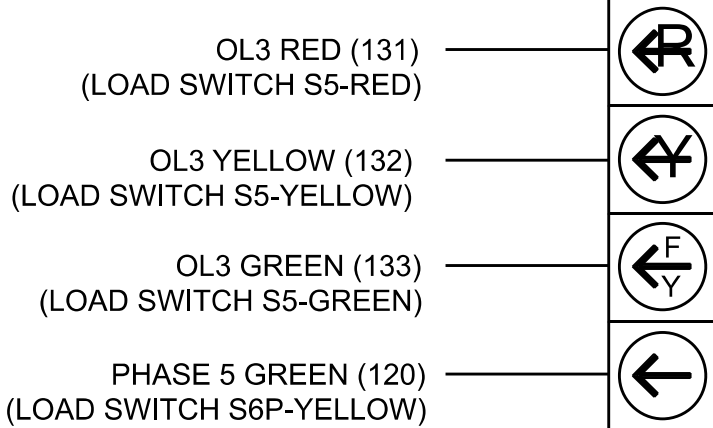
LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	11	7	8
PHASE	1	2	2 PED	OL5	4	4 PED	OL3	6	6 PED	5 GRN	7	8 PED
SIGNAL HEAD NO.	NU	21,22	P21, P22	42	41,42	P41, P42	51	61,62	P61, P62	51	81,82	P81, P82
RED		128		*	101			134			107	
YELLOW		129			102			135			108	
GREEN		130			103			136			109	
RED ARROW								131				
YELLOW ARROW				117				132				
FLASHING YELLOW ARROW								133				
HAND			113			104			119			110
GREEN ARROW				118						120		
WALK			115			106			121			112

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.
* See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



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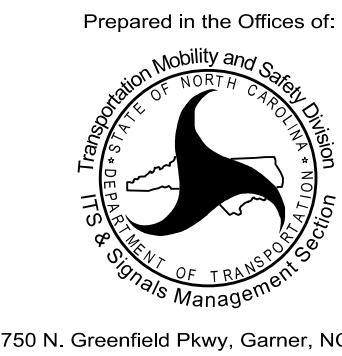
COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 14-0402
DESIGNED: October 2025
SEALED: 10/23/2025
REVISED: N/A

Electrical Detail - Sheet 1 of 2

Electrical and Programming
Details For:



750 N. Greenfield Pkwy, Garner, NC 27529

US 19 (Main Street)
at
SR 1364 (Everett Street)/
Rector Street

Division 14 Swain County Bryson City

PLAN DATE: October 2025

REVIEWED BY:

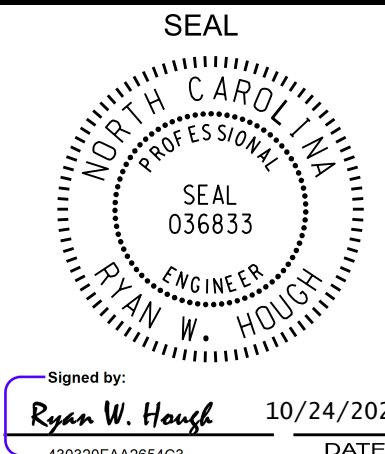
PREPARED BY: Sarah Kirkpatrick

REVIEWED BY:

REVISIONS

INIT.

DATE



Signed by: Ryan W. Hough

10/24/2025

SIG. INVENTORY NO. 14-0402

SPECIAL DETECTOR NOTE

Install a multizone microwave detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.